

## CSIS Academic Lecture

CSIS Academic Lecture will be held as below.

We welcome many people to participate in this lecture.

Date: August 21 (Wednesday), 2013

Time: 13:30 – 15:00

Place: Room A401, 4F, Laboratory for Nanoelectronics and Spintronics,

Speaker: Prof. Warren J. Gross (McGill University, Canada)

Title: “VLSI Implementation of Associative Memories”

Abstract: Associative memories retrieve stored information given partial or erroneous inputs. Recently, a new family of associative memories based on clustered neural networks was introduced that improves greatly on the previous state-of-the-art. In this talk, I will review sparse clustered neural networks and discuss VLSI architectures for their implementation. An algorithm and architecture for low-power IP lookup based on sparse clustered networks will be presented.

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