



The 2nd CSIS International Symposium on Spintronics-based VLSIs

and

The 8th RIEC International Workshop on Spintronics

Date: February 2nd and 3rd, 2012

Venue: Laboratory for Nanoelectronics and Spintronics,
4th Floor, Conference Room, Tohoku University,
Sendai, Japan

<http://www.csis.tohoku.ac.jp/>

Symposium Registration

Registration: E-mail to CSIS Support Office (sien@csis.tohoku.ac.jp) by January 26th, 2012.

Symposium Fee: Free

Banquet Registration

Advance Registration: E-mail to CSIS Support Office (sien@csis.tohoku.ac.jp) by January 26th, 2012.

Banquet Fee: 5,000 JPY
Banquet Fee will be collected on site at the Symposium

Sponsors

The symposium and the workshop are sponsored and supported by:

- Cabinet Office, Government of Japan
- Ministry of Education, Culture, Sports, Science & Technology, Japan
- Japan Society for Promotion of Science (JSPS) through the “Funding Program for World-Leading Innovative R&D on Science and Technology (the FIRST Program)”
- Research Institute of Electrical Communication (RIEC), Tohoku University

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PROGRAM

Thursday February 2nd, 2012 - Materials and Devices -		
Opening 9:00 -9:10		Opening remarks <u>Hideo Ohno</u> (Tohoku University)
Invited talk 9:10-9:45	T1	Size-dependent sub-volume magnetic excitation in perpendicularly magnetized tunnel junctions and implications for spin-torque-based memory technologies <u>Jonathan Z. Sun</u> (IBM)
Invited talk 9:45-10:05	T2	Thermal stability factor of CoFeB/MgO perpendicular magnetic tunnel junctions <u>Hideo Sato</u> , M. Yamanouchi, K. Miura, S. Ikeda, R. Koizumi, F. Matsukura, and H. Ohno (Tohoku University)
10:05-10:20		Coffee Break
Invited talk 10:20-10:55	T3	Tetragonal Mn-Ga alloys for MRAM application <u>Shigemi Mizukami</u> (Tohoku University)
Invited talk 10:55-11:30	T4	Magnetic properties and tunnel magneto-resistance effect of perpendicularly magnetized L1₀-ordered alloys <u>Mikihiko Oogane</u> , M. Hosoda, H. Saruyama, N. I. Khan, G. Kim, N. Inami, H. Naganuma and Y. Ando (Tohoku University)
Invited talk 11:30-12:05	T5	MRAM concepts for sub-20nm scaling and ultrafast precessional switching <u>Ricardo Sousa</u> ¹ , S. Bandiera ¹ , M. Marins de Castro ¹ , B. Lacoste ¹ , C. Ducruet ² , S. Auffret ¹ , I. L. Prejbeanu ² , U. Ebels ¹ , C. Portemont ² , L.D. Buda-Prejbeanu ¹ , B. Rodmacq ¹ , and B. Dieny ¹ (¹ SPINTEC, ² Crocus)
12:05-14:00		Lunch
14:00-15:30		POSTER SESSION See pages 5 and 6
Invited talk 15:30-16:05	T6	STT-MRAM development with 300-mm facilities at LEAP <u>Toshihiro Sugii</u> , M. Aoki, H. Noshiro, T. Ochiai, K. Tsunoda, Y. Iba, A. Hatada, M. Nakabayashi, Y. Yamazaki, A. Takahashi, and C. Yoshida (LEAP)
Invited talk 16:05-16:25	T7	Magnetic resonance and switching induced by the Spin Hall effect <u>Luqiao Liu</u> ¹ , C. F. Pai ¹ , O. J. Lee ¹ , T. J. Gudmundsen ¹ , D. C. Ralph ^{1,2} and R. A. Buhrman ¹ (¹ Cornell University, ² Kavli Institute at Cornell)

Invited talk 16:25-17:00	T8	Current induced magnetization dynamics in CoFeB/MgO nanostructures <u>Masamitsu Hayashi</u> ¹ , J. Kim ¹ , J. Sinha ¹ , S. Mitani ¹ , Y. Nakatani ² S. Fukami ³ , M. Yamanouchi ³ and H. Ohno ³ (¹ NIMS, ² University of Electro-Communications, ³ Tohoku University)
17:00-17:15		Coffee Break
Invited talk 17:15-17:50	T9	Current-induced domain wall motion in the presence of spin-orbit coupling <u>Kyung-Jin Lee</u> ^{1,2,3} , S.-M. Seo ¹ , K.-W. Kim ⁴ , J. Ryu ⁴ , and H.-W. Lee ⁴ (¹ Korea University, ² NIST, ³ University of Maryland, ⁴ Pohang University of Science and Technology)
Invited talk 17:50-18:25	T10	Magnetoresistance and spin-transfer torque in magnetic tunnel junctions <u>Shinji Yuasa</u> ^{1,2} , K.Yakushiji ¹ , H.Kubota ¹ , A.Fukushima ¹ and K.Ando ¹ (¹ AIST, ² IEEE Distinguished Lecturer)
Invited talk 18:25-19:00	T11	Racetrack Memory 2.0: a high-performance, storage class memory using perpendicularly magnetized domain-walls manipulated by current <u>Stuart Parkin</u> (IBM)
Banquet 19:30-21:00		Barbaresco (Italian restaurant), Sendai Trust City Plaza, 1 st floor http://www.stillfoods.com/barbaresco/location.html

Friday February 3rd, 2012

Spintronics-based LSIs

Invited talk 9:00-9:35	F1	Restructuring of memory hierarchy in electrical system and no-standby-power nonvolatile logic with STT-RAM technology <u>Tetsuo Endoh</u> (Tohoku University.)
Invited talk 9:35-10:10	F2	STT MRAM: Recent strong semiconductor Industry traction <u>Yiming Huai</u> , Y. Zhou, Z. Wang, R. Malmhall, R. Ranjan and J. Zhang (Avalanche Technology)
Invited talk 10:10-10:45	F3	Embedded STT-MRAM for advanced mobile system-on-chips: Current status and prospects <u>Seung Kang</u> (Qualcomm)
10:45-11:00		Coffee Break
Invited talk 11:00-11:35	F4	SPRAM (SPin-transfer torque RAM) technology for environmentally-friendly social innovation <u>Masanori Odaka</u> (Hitachi)
Invited talk 11:35-12:10	F5	Design considerations for 1T-1STT MTJ based embedded memory arrays <u>Arijit Raychowdhury</u> (Intel)
12:10-14:00		Lunch
Invited talk 14:00-14:35	F6	Proposal of new MTJ-based nonvolatile memories <u>Takashi Ohsawa</u> , Hiroki Koike, Shoji Ikeda, Takahiro Hanyu, Hideo Ohno, and Tetsuo Endoh (Tohoku University)
Invited talk 14:35-14:55	F7	A content addressable memory using three-terminal magnetic domain wall motion cells <u>Ryuuke Nebashi</u> ¹ , N. Sakimura ¹ , Y. Tsuji ¹ , S. Fukami ² , H. Honjo ¹ , S. Saito ¹ , S. Miura ¹ , N. Ishiwata ¹ , K. Kinoshita ² , T. Hanyu ² , T. Endoh ² , N. Kasai ² , H. Ohno ² , and T. Sugabayashi ¹ (¹ NEC, ² Tohoku University)
Invited talk 14:55-15:15	F8	High-density ternary content-addressable memory using MTJ-based nonvolatile logic-in-memory architecture <u>Takahiro Hanyu</u> , (Tohoku University)
15:15-15:30		Coffee Break
Panel Discussion 15:30-17:00		“Spintronics-based VLSIs, What remains to be done?” Moderators : Hideo Ohno (Tohoku University), Tetsuo Endoh (Tohoku University) Panelists : Yasuo Ando(Tohoku University), Yiming Huai(Avalanche), Seung Kang(Qualcomm), Shoji Ikeda(Tohoku University), Kyung-Jin Lee(Korea University), Stuart Parkin(IBM), Ricardo Sousa(Spintec), Jonathan Sun(IBM), Takahiro Hanyu(Tohoku University), Tadahiko Sugabayashi(NEC), Toshihiro Sugii(LEAP)
Closing 17:00-17:05		Closing remarks

Thursday February 2nd, 2012

POSTER SESSION 14:00-15:30

P1	Effects of rapid thermal annealing on magnetic coupling of chromium-doped indium zinc oxides <u>C.Y. Hsu</u> (National Sun Yat-Sen University)
P2	Magneto-optical studies of Co doped amorphous carbon films <u>H. S. Hsu</u> ¹ , <u>W. Y. Su</u> ^{1,4} , <u>J. S. Lee</u> ¹ , <u>S. J. Sun</u> ² , <u>P. Y. Chuang</u> ³ , and <u>C. H. Lee</u> ³ (¹ National Pingtung University of Education, ² National University of Kaohsiung, ³ National Tsing-Hua University, ⁴ National Kaohsiung Normal University)
P3	Annealing temperature dependence of tunnel magnetoresistance in MgO magnetic tunnel junctions with thin CoFeB electrodes <u>H. D. Gan</u> ¹ , <u>S. Ikeda</u> ¹ , <u>M. Yamanouchi</u> ¹ , <u>H. Sato</u> ¹ , <u>K. Miura</u> ^{1,2} , <u>K. Mizunuma</u> ¹ , <u>R. Koizumi</u> ¹ , <u>F. Matsukura</u> ¹ , and <u>H. Ohno</u> ¹ (¹ Tohoku University, ² Hitachi)
P4	B concentration dependence of magnetic anisotropy in MgO/CoFeB/Ta stack structure <u>R. Koizumi</u> ¹ , <u>S. Ikeda</u> ¹ , <u>H. Sato</u> ¹ , <u>M. Yamanouchi</u> ¹ , <u>K. Miura</u> ^{1,2} , <u>K. Mizunuma</u> ¹ , <u>H. D. Gan</u> ¹ , <u>F. Matsukura</u> ¹ and <u>H. Ohno</u> ¹ (¹ Tohoku University, ² Hitachi)
P5	Magnetotransport properties in perpendicular magnetized L1₀-CoPt/CoFeB and L1₀-FePd/CoFeB magnetic tunnel junctions <u>H. Naganuma</u> , <u>G. Kim</u> , <u>M. Oogane</u> , and <u>Y. Ando</u> (Tohoku University)
P6	Crystal symmetry change from rhombohedral and tetragonal by substrate temperature for BiFeO₃ epitaxial films <u>H. Naganuma</u> , <u>K. Mukaiyama</u> , <u>M. Oogane</u> , and <u>Y. Ando</u> (Tohoku University)
P7	Effect of device temperature on domain wall motion in a perpendicularly magnetized Co/Ni wire <u>H. Tanigawa</u> , <u>K. Suemitsu</u> , <u>Y. Ozaki</u> , <u>T. Suzuki</u> , and <u>E. Kariyada</u> (Renesas Electronics)
P8	Energy-assisted oxidation process of Mg layer for MgO-MTJs <u>H. Yamamoto</u> ¹ , <u>T. Morita</u> ¹ , <u>S. Ikeda</u> ² , and <u>H. Ohno</u> ² (¹ ULVAC, ² Tohoku University ²)
P9	Spin torque diode effect of perpendicularly magnetized CoFeB/MgO/CoFeB magnetic tunnel junctions <u>N. Inami</u> , <u>H. Naganuma</u> , <u>M. Oogane</u> , <u>Y. Ando</u> , <u>S. Ikeda</u> and <u>H. Ohno</u> (Tohoku University)
P10	Tailoring nonmagnetic-metals/Fe interfaces with perpendicular magnetic anisotropy: A first-principles study <u>Y. Miura</u> , <u>M. Tsujikawa</u> , <u>K. Abe</u> and <u>M. Shirai</u> (Tohoku University)
P11	Ultrafast write operation of reconfigurable spin logic block with SPRAM for 3D-stacked reconfigurable spin processor <u>R. Nakazawa</u> ¹ , <u>H. Kino</u> ¹ , <u>K. Kiyoyama</u> ² , <u>T. Tanaka</u> ¹ , and <u>M. Koyanagi</u> ¹ (¹ Tohoku University, ² Nagasaki Institute of Applied Science)
P12	Ab initio study on magnetic anisotropy of L1₀-FeNi and FeNi multilayers <u>S. Ozaki</u> , <u>Y. Kuwahara</u> , <u>M. Tsujikawa</u> , <u>Y. Miura</u> , <u>K. Abe</u> and <u>M. Shirai</u> (Tohoku University)

P13	Hole concentration dependence of the Curie temperature of (Ga,Mn)Sb channel in field-effect structure <u>S. Akita</u> , F. Matsukura and H. Ohno (Tohoku University)
P14	Spin-transfer switching characteristics in magnetic tunnel junctions with synthetic free layers <u>M. Nishimura</u> , <u>M. Oogane</u> , H. Naganuma, N. Inami and Y. Ando (Tohoku University)
P15	First principles study of interface magnetic anisotropy between transition metal nitride (carbide) and Fe(001) <u>M. Tsujikawa</u> , Y. Miura and M. Shirai (Tohoku University)
P16	Perpendicular magnetic anisotropy of Co₂FeAl/MgO interfaces <u>D. Mori</u> , M. Tujikawa, Y. Miura, K. Abe and M. Shirai (Tohoku University)
P17	7T-2MTJ-Based High-Speed Nonvolatile Fully Parallel TCAM <u>S. Matsunaga</u> , A. Katsumata, M. Natsui, and T. Hanyu (Tohoku University)
P18	Design of a Compact MTJ/MOS-Hybrid Logic Element for a Nonvolatile Field-Programmable Gate Array <u>D. Suzuki</u> and T. Hanyu (Tohoku University)
P19	Magnetic proximity effect of Co₂FeAl/(Ga,Mn)As bilayers S. H. Nie ^{1,2} , K. K. Meng ¹ , L. Chen ¹ , <u>L. J. Zhu</u> ¹ , X. Z. Yu ¹ , H. L. Wang ¹ , W. S. Yan ³ , Y. G. Zhao ² , and J. H. Zhao ¹ (¹ Chinese Academy of Sciences, ² Tsinghua University, ³ University of Science & Technology of China)
P20	Nonvolatile Low Power 16-bit/32-bit Binary Counter with MTJ and its Scalability <u>S. Togashi</u> , T. Ohsawa and T. Endoh (Tohoku University)
P21	A Study for Adopting PMOS Memory Cell for 1T1R STT-RAM with Asymmetric Switching Current MTJ <u>H. Koike</u> , T. Ohsawa and T. Endoh (Tohoku University)