

# CSIS Symposium Day I

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International symposium on

## Spintronics for Integrated Circuit Applications and Beyond

March 13, 2014 Tokyo, Japan

Tokyo International Forum, Hall D7

<http://www.t-i-forum.co.jp/en/>

### ◆ Invited Speakers

David Awschalom (University of Chicago)

Tetsuo Endoh (Tohoku University)

Takahiro Hanyu (Tohoku University)

Seung H. Kang (Qualcomm Technologies)

Hideo Ohno (Tohoku University)

Soon Oh Park (Samsung)

Akihisa Sekiguchi (Tokyo Electron)

Registration Dates

JAN. 15. - ~~FEB. 28~~, 2014.

⇒ March 6, 2014



Click Here For Registration Form

### ◆ Registration

The Symposium is free of charge, but registration is required. Download [the application form](#) and e-mail to CSIS support Office (e-mail: [csisconf@riec.tohoku.ac.jp](mailto:csisconf@riec.tohoku.ac.jp)) by ~~Feb. 28~~, ⇒ March 6, 2014.

### ● Banquet

Time: 18:00-19:30 Banquet fee is 7,000JPY.

Venue: Tokyo International Forum, Hall D6.

### Committee and Secretariat

Committee : H. Ohno, T. Endoh, N. Kasai, T. Hanyu, Y. Ando, S. Ikeda, M. Yamanouchi, and H. Sato

Secretariat : Y. Kadowaki, N. Sato, O. Sugasawa, M. Abe, and Y. Jidai (CSIS Support Office)

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### Sponsors/ Organizations

Center for Spintronics Integrated systems (CSIS), Tohoku University

Ministry of Education, Culture, Sports, Science & Technology

The Japan Society for the Promotion of Science (JSPS)

FIRST Program (Core Researcher: Hideo OHNO)

: Research and Development of Ultra-low Power Spintronics-based VLSIs





# CSIS Symposium (Day I)



## The International Meeting on Spintronics

### for Integrated Circuits Applications and Beyond

# PROGRAM

<b>Thursday, March 13, 2014</b>		
<b>Tokyo International Forum, Hall D7</b>		
Opening 10:30-10:40		<b>Opening remarks</b> <u>Hideo Ohno</u> (Tohoku University)
Invited talk 10:40-11:20	T1	<b>Spintronics: Materials through Devices to Integrated Circuits</b> <u>Hideo Ohno</u> (Tohoku University)
Invited talk 11:20-12:00	T2	<b>Extendibility of CoFeB based perpendicular MTJ technology as high density memory</b> <u>Soon Oh Park</u> (Samsung Electronics)
12:10-14:00	Lunch	
Invited talk 14:00-14:40	T3	<b>Perspective in STT-MRAM Market and Technology</b> <u>Akihisa Sekiguchi</u> (Tokyo Electron)
Invited talk 14:40-15:20	T4	<b>Spintronics-based Nonvolatile Computing Systems</b> <u>Tetsuo Endoh</u> (Tohoku University)
Invited talk 15:20-16:00	T5	<b>Embedded STT-MRAM: Paths to Products</b> <u>Seung H. Kang</u> (Qualcomm Technologies)
16:00-16:20	Coffee Break	
Invited talk 16:20-17:00	T6	<b>Challenge of MTJ/MOS-Hybrid Logic LSI Based on Nonvolatile Logic-in-Memory Architecture for Dark Silicon Applications</b> <u>Takahiro Hanyu</u> (Tohoku University)
Invited talk 17:00-17:40	T7	<b>Quantum Spintronics</b> <u>David Awschalom</u> (The University of Chicago)
Banquet 18:00-19:30	Tokyo International Forum, D block 6F	