

# CSIS Symposium Day II

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Research and Development of Ultra-low Power Spintronics-based VLSIs  
core researcher : Hideo Ohno

## CSIS Research Review March 14, 2014 Tokyo, Japan

Tokyo International Forum, Hall D7

<http://www.t-i-forum.co.jp/en/>

### ◆ Opening Remarks

**Susumu Satomi** (President of Tohoku University)

**Kazuo Kyuma** (Executive Members,  
Council for Science and Technology Policy, Cabinet Office,  
Government of Japan)

### ◆ Overview

**Hideo Ohno** (Tohoku University)

### ◆ Research Report

**Yasuo Ando** (Tohoku University)

**Shoji Ikeda** (Tohoku University)

**Tetsuo Endoh** (Tohoku University)

**Warren Gross** (McGill University, Canada)

**Tadahiko Sugibayashi** (NEC Corporation)

**Takahiro Hanyu** (Tohoku University)

### ◆ Closing Remarks

**Hideo Ohno** (Tohoku University)

[Click Here For Registration Form](#)



Registration Dates  
JAN. 15. - FEB. 28,  
⇒ March 6 2014.

### ◆ Registration

The conference is **free of charge**,  
but registration is required.

Download [the application form](#)  
and e-mail to CSIS support Office  
(e-mail:[csisconf@riec.tohoku.ac.jp](mailto:csisconf@riec.tohoku.ac.jp))  
by ~~Feb. 28~~, ⇒ **March 6 2014**.

### 【Contact Information】

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Funding program for World-Leading Innovative R&D on Science and Technology (FIRST)  
March 10<sup>th</sup>, 2010 – March 31<sup>st</sup>, 2014



# Research and Development of Ultra-low Power Spintronics-based VLSIs

core researcher : Hideo Ohno  
Center for Spintronics Integrated Systems, Tohoku University

Granted by the Japan Society for the Promotion of Science (JSPS) through the “Funding Program for World-Leading Innovative R&D on Science and Technology (FIRST Program),” initiated by the Council for Science and Technology Policy (CSTP).

## CSIS Research Review

Friday 14<sup>th</sup> March, 2014

Tokyo International Forum Hall D7

Access :

<http://www.t-i-forum.co.jp/en/access/>

(Hall D7) <http://www.t-i-forum.co.jp/en/facilities/>

### Program

9:20	Opening Remarks (1)	Susumu Satomi (President of Tohoku University)
9:30	Opening Remarks (2)	Kazuo Kyuma (Executive Members, Council for Science and Technology Policy, Cabinet Office, Government of Japan )
9:40	Overview	Hideo Ohno (Core Researcher, Tohoku University)
10:30	Spintronics materials and devices development I <a href="#">Progress of perpendicular anisotropy CoFeB-MgO based magnetic tunnel junctions</a>	Shoji Ikeda (Tohoku University.)
11:05	Spintronics materials and devices development II <a href="#">L1<sub>0</sub>-ordered alloys, innovative materials and device technology for magnetic tunnel junctions</a>	Yasuo Ando (Tohoku University.)
11:40	Lunch Break	
14:00	Spintronics logic design and IP development I <a href="#">Spintronics logic design and IP development Based on Nonvolatile Logic-in-Memory Architecture</a>	Takahiro Hanyu (Tohoku University)
14:35	Spintronics logic design and IP development II <a href="#">VLSI Implementation of Associative Memories</a>	Warren Gross (Invited) (McGill University, Canada)
15:10	Spintronics logic design and IP development III <a href="#">Fully Nonvolatile Microcontroller Utilizing Three Terminal MTJ for Standby-Power-Critical Applications</a>	Tadahiko Sugibayashi (NEC Corporation)
15:45	Spintronics logic design and IP development IV <a href="#">MTJ-based Logic &amp; Working Memory and Its Design Automation Tools</a>	Tetsuo Endoh (Tohoku University)
16:20	Closing Remarks	Hideo Ohno (Core Researcher, Tohoku University)

#### 【 Contact information 】

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