

The 2nd CSIS International Symposium on

Spintronics-based VLSIs

and

The 8th RIEC International Workshop on

Spintronics

2-3 Feb. 2012 Sendai, Japan

Conference Room, Laboratory for Nanoelectronics and Spintronics

Research Institute of Electrical Communication, Tohoku University

URL: <http://www.csis.tohoku.ac.jp/english/index.html>

◆ Invited Speakers

2 February, Thursday

Jonathan Sun (IBM)

Hideo Sato (Tohoku University)

Shigemi Mizukami (Tohoku University)

Mikihiko Oogane (Tohoku University)

Ricardo Sousa (Spintec)

Toshihiro Sugii (LEAP)

3 February, Friday

Tetsuo Endoh (Tohoku University)

Yiming Huai (Avalanche)

Seung Kang (Qualcomm)

Masanori Odaka (Hitachi)

Masamitsu Hayashi (NIMS)

Kyung-Jin Lee (Korea University)

Shinji Yuasa (AIST)

Stuart Parkin (IBM)

Arijit Raychowdhury (Intel)

Takashi Ohsawa (Tohoku University)

Ryusuke Nebashi (NEC)

Takahiro Hanyu (Tohoku University)

◆ Poster Session

The committee welcomes poster submissions on all aspects of spintronics materials, devices, process, and circuit design presented for Poster Session on February 2nd. Prospective authors must send their submission to CSIS Support Office (e-mail: sien@csis.tohoku.ac.jp) by January 10th, 2012 by e-mail. Download the format from the top page of this site. Authors of accepted posters will be notified by January 13th, 2012.

◆ Panel Discussion

3 February, Friday 15:30-17:00 “Spintronics-based VLSIs, what remains to be done?”

◆ Registration

Registration fee for the Symposium and Workshop is “FREE”. Banquet fee is 5,000 JPY. Download the application form from the top page of this site and e-mail to CSIS support Office (e-mail: sien@csis.tohoku.ac.jp) by January 26th, 2012.

◆ Committee and Secretariat

Committee : H. Ohno, T. Endoh, N. Kasai, T. Hanyu, Y. Ando, S. Ikeda, M. Yamanouchi, and H. Sato

Secretariat : Y. Kadowaki, N. Sato, O. Sugasawa, M. Abe, and Y. Jidai (CSIS Support Office)

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◆ Sponsors/ Organizations

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